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IPM B2

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Attorney's Docket No. ITW 8637.62

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Steven J. Geissler  
Application No.: 10/826534  
Group Art Unit: 2838  
Filed: April 16, 2004  
For: Method And Apparatus For Receiving A Universal Input Voltage  
In A Welding, Plasma Or Heating Power Source

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE  
STATEMENT BEFORE MAILING DATE OF EITHER  
A FINAL ACTION OR A NOTICE OF ALLOWANCE

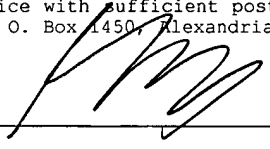
TIME OF TRANSMITTAL OF ACCOMPANYING  
INFORMATION DISCLOSURE STATEMENT

1. The information disclosure statement transmitted herewith is being filed **after** three months of the filing date of this national application or the date of entry of the national stage as set forth in § 1.491 in an international application or after the mailing date of the first Office action on the merits, whichever event occurred last but before the mailing date of either:
  - (1) a final action under Section 1.113 or
  - (2) a notice of allowance under Section 1.311, whichever occurs first.

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Date: July 7, 2005

  
George R. Corrigan

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### **CERTIFICATION OR FEE**

2. Accompanying this transmittal is the fee set forth in 37 C.F.R. 1.17(p) for submission of an information disclosure statement under Section 1.97(c). s (\$180.00).

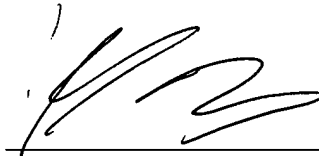
### **FEE PAYMENT**

3. Applicant elects the option to pay the fee set forth in 37 C.F.R. 1.17(p) for submission of an information disclosure statement under Section 1.97(c) (\$180.00).

Fee due = \$ 180.00

### **METHOD OF PAYMENT OF FEE**

4. Please charge the fee of 180.00 and any additional fees that may be due, to the credit card specified on the attached sheet.



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**George R. Corrigan**  
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5 Briarcliff Court  
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Sheet 1 of 1.

Form PTO-1449	U.S. Department of Commerce Patent And Trademark Office	ATTY. DOCKET NO. ITW 8637.62	SERIAL NO. 10/826534
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION		APPLICANT Steven J. Geissler	
		FILING DATE April 16, 2004	GROUP 2838

## U.S. PATENT DOCUMENTS

EXAM. INIT.	REF	PATENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
		4,564,742	1/14/86	Karlsson			
		4,683,529	7/28/87	Bucher, II			
		5,086,205	2/4/92	Thommes			
		5,149,935	9/22/92	Ozimek et al.			
		5,444,356	8/22/95	Reynolds et al.			
		5,563,777	10/8/96	Miki et al.			
		6,002,103	12/14/99	Thommes			
		6,239,407	5/29/01	Thommes			
		6,815,639	11/6/04	Geissler			
		6,849,827	2/1/05	Thommes			

## FOREIGN PATENT DOCUMENTS

EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO
		DE4128175	93/02/25	Germany				
		DE4211906	93/06/09	Germany				
		DE4411227	95/10/05	Germany				
		GB2258958	93/02/24	Great Britain				

## OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)

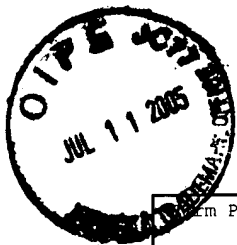
	Unitrode Corporation, Boost Power Factor Corrector Design With The UC3853, By Philip C. Todd
	Design Issues For A Zero-Switched Power Factor Correction Circuit and DC/DC Converter Power Processing Unit, Y.V. Panov, J.A. Sabate, and F.C. Lee 1993 VPEC Reprinted with permission from <u>Proceedings of the Virginia Power Electronics Seminar</u> , Blackburg, VA; September 19-21, 1993; pp. 213-224
EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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\*DN\*F:\WP51\DOCS\ITW\8637.62\8637.621449IDS1.wpd Wednesday, June 29, 2005 3:38PM



Sheet 1 of 1.

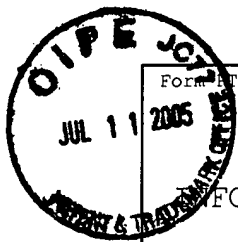
Form PTO-1449		U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 8637.62		SERIAL NO. 10/826534	
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION				APPLICANT Steven J. Geissler			
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FOREIGN PATENT DOCUMENTS							
EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
		S63-190646	90-2-09	Japan			
		S63-42690	89-08-08	Japan			
		JP59220285	84-12-11	Japan			
		JP62107868	87-05-19	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Unitrode Corporation A High Performance Linear Regulator For Low Dropout Applications Dave Zendzian Applications Engineer					
		Unitrode Corporation, High Power Factor Switching Preregulator Design Optimization, Lloyd Dixon					
		Miller ® Maxstar 200 STR Owners Manual October 2003					
		A Novel Single-Phase Power Factor Correction Scheme Y.M. Jiang, F.C. Lee, G.C. Hua, and W. Tang 1993 VPEC Reprinted with permission from <u>Proceedings of the Applied Power Electronics Conference</u> : San Diego, CA; March 7-11, 1993; pp. 287- 292					
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OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Unitrode , Application Note, Power Factor Correction Using The UC3852 Controlled On-Time Zero Current Switching Technique, Bill Andreyckak					
		Reduction of Voltage Stress in Intrgrated High-Quality Rectifier-Regulators by Variable-Frequency Control, M.M. Jovanovic, D.M. Tsang, and F.C. Lee, 1994 VPEC. Reprinted with permission from <u>Proceedings of the Applied Power Electronics Conference</u> ; Orlando, FL; February 13-17, 1994; pp. 569- 575.					
		Single-Stage Single-Phase Parallel Power Factor Correction Scheme, Y. Jiang and F.C. Lee 1994 VPEC. Reprinted with permission from <u>Proceedings of the Power Electronics Specialist Conference</u> ; Taipei, Taiwan; June 20-25, 1994					
		Miller ® <i>The Power of Blue</i> , Spectrum 2050, Owners Manual November 1999					
		Unitrode, Application Note, UC3854 Controlled Power Factor Correction Circuit Design, Philip C. Todd					
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